

Refine Search

Search Results -

Terms	Documents
L25 or L24 or L26	24

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
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 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L27  

Search History

DATE: Monday, May 03, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L27</u>	L25 or L24 or L26	24	<u>L27</u>
<u>L26</u>	L6 and L7	14	<u>L26</u>
<u>L25</u>	L6 and L8	2	<u>L25</u>
<u>L24</u>	L23 or L21 or L20 or L17	21	<u>L24</u>
<u>L23</u>	L14 and L7	3	<u>L23</u>
<u>L22</u>	L14 and L8	0	<u>L22</u>
<u>L21</u>	L19 and L8	1	<u>L21</u>
<u>L20</u>	L19 and L7	6	<u>L20</u>
<u>L19</u>	L11 and L10	48	<u>L19</u>
<u>L18</u>	L14 and L10	0	<u>L18</u>
<u>L17</u>	L11 and L6	12	<u>L17</u>
<u>L16</u>	L13 and L6	0	<u>L16</u>
<u>L15</u>	L14 and L6	0	<u>L15</u>
<u>L14</u>	L13 and L5	73	<u>L14</u>

<u>L13</u>	L12 and l2	136	<u>L13</u>
<u>L12</u>	l1 and l3	193	<u>L12</u>
<u>L11</u>	l1 and l2	4005	<u>L11</u>
<u>L10</u>	routing same multiplexer\$1	4247	<u>L10</u>
<u>L9</u>	(semiconductor memory) same (fuse\$1 or antifuse\$1 or eeprom\$1)	7690	<u>L9</u>
<u>L8</u>	711/\$.ccls.	21094	<u>L8</u>
<u>L7</u>	714/\$.ccls.	44682	<u>L7</u>
<u>L6</u>	(redundan\$5 near3 row\$1) same (rout\$5 near3 circuit\$5)	29	<u>L6</u>
<u>L5</u>	compar\$5 same ((row\$1 or column\$1) near3 address\$4)	5747	<u>L5</u>
<u>L4</u>	nonvolatile same (block repair\$5)	0	<u>L4</u>
<u>L3</u>	nonvolatile same (defective near3 cell\$1)	243	<u>L3</u>
<u>L2</u>	array\$2 same (redundan\$5)	15913	<u>L2</u>
<u>L1</u>	semiconductor\$1 near3(memory or memories)	108872	<u>L1</u>

END OF SEARCH HISTORY